



PATENT NUMBER

U.S. UTILITY Patent Application

JC2
JCS45

M.F	O.I.P.E.	PATENT DATE
SCANNED <u>Am 2</u>	Q.A. <u>UR</u>	

APPLICATION NO.	CONT/PRIOR	CLASS <u>715</u> <u>326</u>	SUBCLASS <u>27</u>	ART UNIT	EXAMINER <u>Lee</u> <u>S. CHAN</u>
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APPlicants

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Digital system of adjusting delays on circuit boards

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

□ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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